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Magnitude comparator

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The 74HC688 is an 8-bit magnitude comparator. It performs comparisons of two 8-bit binary or BCD words. Inputs include clamp diodes. This enables the use of current limiting resistors to interface inputs to voltages in excess of V_{CC} .

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8-bit magnitude comparator 74HC/HCT688 AC WAVEFORMS Fig.6 Waveforms showing the enable input (E) to the equal to output (P = Q) propagation delays and the output transition times. (1) HC

: VM = 50%; VI = GND
to VCC. HCT: VM = 1.3
V; VI = GND to 3 V.

Fig.7 Waveforms
showing the word
inputs (P n, Qn) to the
equal to output (P = Q)
propagation delays and
the output transition
times. (1) HC : VM ...

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74HC85D - The
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comparator that can be
expanded to almost
any length. They
perform comparison of
two 4-bit binary, BCD

or other monotonic codes and present the three possible magnitude results at the outputs ($QA > B$, $QA = B$ and $QA < B$). The 4-bit inputs are weighted (A_0 to A_3 and B_0 to B_3), where A_3 and B_3 are the most significant bits.

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4-bit magnitude comparator Rev. 3 — 20 April 2018 Product data sheet 1 General description The 74HC85; 74HCT85 is a 4-bit magnitude comparator that can be expanded to almost any length. They perform comparison of two 4-bit binary, BCD or other monotonic codes and present the three possible magnitude results at the outputs ($QA > B$, $QA = B$ and $QA < B$). The 4-bit inputs are weighted (A_0 to A_3 and B_0 ...

4-bit magnitude comparator - Nexperia

The 74HCT688N is a 8-bit CMOS Magnitude Comparator pin compatible with low power Schottky TTL (LSTTL). It performs comparison of two 8-bit

binary or BCD words.
The output provides $P \setminus = Q \setminus$. Compare two 8-bit words

74HCT688N - Digital Comparator, 74HCT688, Magnitude, 8 ...

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Two or more "688" 8-bit magnitude comparators may be cascaded to compare binary or BCD numbers of more than 8 bits. An example is shown in Fig.8. PACKAGE OUTLINES See "74HC/HCT/HCU/HCMO S Logic Package Outlines". Fig.8 Binary or BCD comparator.

8-bit magnitude comparator - RS Components

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 four bit numbers and
 three outputs to
 generate less than,
 equal to and greater
 than between two
 binary numbers. In a 4-
 bit comparator the
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