
Digital Integrated Circuits A Design Perspective

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Digital Integrated Circuit Design

From VLSI Architectures to CMOS Fabrication *Cambridge University Press*

Top-down approach to practical, tool-independent, digital circuit design, reflecting how circuits are designed.

Digital Integrated Circuits

Analysis and Design, Second Edition *CRC Press*

Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and work. The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years. The second edition of Digital Integrated Circuits: Analysis and Design focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come. Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication, this book delves into the dramatic advances in the field, including new applications and changes in the physics of operation made possible by relentless miniaturization. This book was conceived in the versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering VLSI design and fabrication as a separate topic. Like the first edition, this volume is a crucial link for integrated circuit engineers and those studying the field, supplying the cross-disciplinary connections they require for guidance in more advanced work. For pedagogical reasons, the author uses SPICE level 1 computer simulation models but introduces BSIM models that are indispensable for VLSI design. This enables users to develop a strong and intuitive sense of device and circuit design by drawing direct connections between the hand analysis and the SPICE models. With four new chapters, more than 200 new illustrations, numerous worked examples, case studies, and support provided on a dynamic website, this text significantly expands concepts presented in the first edition.

Digital Integrated Circuits

A Design Perspective

Beginning with discussions on the operation of electronic devices and analysis of the nucleus of digital design, the text addresses: the impact of interconnect, design for low power, issues in timing and clocking, design methodologies, and the effect of design

automation on the digital design perspective.

Digital Integrated Circuit Design Using Verilog and Systemverilog *Elsevier*

For those with a basic understanding of digital design, this book teaches the essential skills to design digital integrated circuits using Verilog and the relevant extensions of SystemVerilog. In addition to covering the syntax of Verilog and SystemVerilog, the author provides an appreciation of design challenges and solutions for producing working circuits. The book covers not only the syntax and limitations of HDL coding, but deals extensively with design problems such as partitioning and synchronization, helping you to produce designs that are not only logically correct, but will actually work when turned into physical circuits. Throughout the book, many small examples are used to validate concepts and demonstrate how to apply design skills. This book takes readers who have already learned the fundamentals of digital design to the point where they can produce working circuits using modern design methodologies. It clearly explains what is useful for circuit design and what parts of the languages are only software, providing a non-theoretical, practical guide to robust, reliable and optimized hardware design and development. Produce working hardware: Covers not only syntax, but also provides design know-how, addressing problems such as synchronization and partitioning to produce working solutions Usable examples: Numerous small examples throughout the book demonstrate concepts in an easy-to-grasp manner Essential knowledge: Covers the vital design topics of synchronization, essential for producing working silicon; asynchronous interfacing techniques; and design techniques for circuit optimization, including partitioning

Digital Integrated Circuits

Design-for-Test Using Simulink and Stateflow *CRC Press*

A current trend in digital design-the integration of the MATLAB® components Simulink® and Stateflow® for model building, simulations, system testing, and fault detection-allows for better control over the design flow process and, ultimately, for better system results. Digital Integrated Circuits: Design-for-Test Using Simulink® and Stateflow® illustrates the construction of Simulink models for digital project test benches in certain design-for-test fields. The first two chapters of the book describe the major tools used for design-for-test. The author explains the process of Simulink model building, presents the main library blocks of Simulink, and examines the development of finite-state machine modeling using Stateflow diagrams. Subsequent chapters provide examples of Simulink modeling and simulation for the latest design-for-test fields, including combinational and sequential circuits, controllability, and observability; deterministic algorithms; digital circuit dynamics; timing verification; built-in self-test (BIST) architecture; scan cell operations; and functional and diagnostic testing. The book also discusses the automatic test pattern generation (ATPG) process, the logical determinant theory, and joint test action group (JTAG) interface models. Digital Integrated Circuits explores the possibilities of MATLAB's tools in the development of application-specific integrated circuit (ASIC) design systems. The book shows how to incorporate Simulink and Stateflow into the process of modern digital design.

Digital Integrated Circuits

Analysis and Design, Second Edition *CRC Press*

Exponential improvement in functionality and performance of digital integrated circuits has revolutionized the way we live and

work. The continued scaling down of MOS transistors has broadened the scope of use for circuit technology to the point that texts on the topic are generally lacking after a few years. The second edition of Digital Integrated Circuits: Analysis and Design focuses on timeless principles with a modern interdisciplinary view that will serve integrated circuits engineers from all disciplines for years to come. Providing a revised instructional reference for engineers involved with Very Large Scale Integrated Circuit design and fabrication, this book delves into the dramatic advances in the field, including new applications and changes in the physics of operation made possible by relentless miniaturization. This book was conceived in the versatile spirit of the field to bridge a void that had existed between books on transistor electronics and those covering VLSI design and fabrication as a separate topic. Like the first edition, this volume is a crucial link for integrated circuit engineers and those studying the field, supplying the cross-disciplinary connections they require for guidance in more advanced work. For pedagogical reasons, the author uses SPICE level 1 computer simulation models but introduces BSIM models that are indispensable for VLSI design. This enables users to develop a strong and intuitive sense of device and circuit design by drawing direct connections between the hand analysis and the SPICE models. With four new chapters, more than 200 new illustrations, numerous worked examples, case studies, and support provided on a dynamic website, this text significantly expands concepts presented in the first edition.

Analysis And Design Of Digital Integrated Circuits, In Deep Submicron Technology (special Indian Edition) *Tata McGraw-Hill Education*

CMOS Digital Integrated Circuits

Analysis and Design

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.

Analysis and Design of Digital Integrated Circuits *McGraw-Hill Science, Engineering & Mathematics*

This is a state-of-the-art treatment of the circuit design of digital integrated circuits. It includes coverage of the basic concepts of static characteristics (voltage transfer characteristics, noise margins, fanout, power dissipation) and dynamic characteristics (propagation delay times) and the interrelationships among these parameters. The authors are regarded as leading authorities in integrated circuits and MOS technology.

Digital Integrated Circuit Design *Oxford University Press on Demand*

The impact of digital integrated circuits on our modern society has been pervasive. They are the enabling technology of the current computer and information-technology revolution. This is largely true because of the immense amount of signal and computer processing that can be realized in a single integrated circuit; modern IC's may contain millions of logic gates. This text book is intended to take a reader having only a minimal background and knowledge in electronics to the point where they can design state-of-the-art digital integrated circuits. Designing high-performance digital integrated circuits requires expertise in many different areas. These include semiconductor physics, integrated circuit processing, transistor-level design, logic-level design, system-level design, testing, etc. Aspects of these topics are covered throughout this text, although the emphasis is on transistor-level design of digital integrated circuits and systems. This is in contrast to the perspective in many other texts, which takes a system-level or VLSI approach where transistor-level details are minimized. It is the author's belief that before system-level considerations can be properly evaluated, an in-depth transistor-level understanding must first be obtained. Important system-level considerations such as timing, pipe-lining, clock distribution, and system building blocks are covered in detail, but the emphasis on transistors first. Throughout the book, physical and intuitive explanations are given, and although mathematical quantitative analysis of many circuits have necessarily been presented, Martin has attempted not to "miss seeing the forest because of the trees". This book presents the critical underlying concepts without becoming entangled in tedious and over-complicated circuit analyses. It is intended for senior/graduate level students in electrical and computer engineering. This course assumes the Sedra/Smith Microelectronic Circuits course as a prerequisite.

Three-dimensional Integrated Circuit Design *Morgan Kaufmann*

With vastly increased complexity and functionality in the "nanometer era" (i.e. hundreds of millions of transistors on one chip), increasing the performance of integrated circuits has become a challenging task. Connecting effectively (interconnect design) all of these chip elements has become the greatest determining factor in overall performance. 3-D integrated circuit design may offer the best solutions in the near future. This is the first book on 3-D integrated circuit design, covering all of the technological and design aspects of this emerging design paradigm, while proposing effective solutions to specific challenging problems concerning the design of 3-D integrated circuits. A handy, comprehensive reference or a practical design guide, this book provides a sound foundation for the design of 3-D integrated circuits. * Demonstrates how to overcome "interconnect bottleneck" with 3-D integrated circuit design...leading edge design techniques offer solutions to problems (performance/power consumption/price) faced by all circuit designers * The FIRST book on 3-D integrated circuit design...provides up-to-date information that is otherwise difficult to find * Focuses on design issues key to the product development cycle...good design plays a major role in exploiting the implementation flexibilities offered in the 3-D * Provides broad coverage of 3-D integrated circuit design, including interconnect prediction models, thermal management techniques, and timing optimization...offers practical view of designing 3-D circuits

Practical Design of Digital Circuits

Basic Logic to Microprocessors *Elsevier*

Practical Design of Digital Circuits: Basic Logic to Microprocessors demonstrates the practical aspects of digital circuit design. The intention is to give the reader sufficient confidence to embark upon his own design projects utilizing digital integrated circuits as soon as possible. The book is organized into three parts. Part 1 teaches the basic principles of practical design, and introduces the designer to his "tools" — or rather, the range of devices that can be called upon. Part 2 shows the designer how to put these together into viable designs. It includes two detailed descriptions of actual design exercises. The first of these is a fairly simple exercise in CMOS design; the second is a much more complex design for an electronic game, using TTL devices. Part 3 focuses on microprocessors. It illustrates how a particular design problem changes emphasis when a microprocessor is introduced. This book is aimed at a fairly broad market: it is intended to aid the linear design engineer to cross the barrier into digital electronics; it should provide interesting supporting reading for students studying digital electronics from the more academic viewpoint; and it should enable the enthusiast to design much more ambitious and sophisticated projects than he could otherwise attempt if restricted to linear devices.

CMOS

Circuit Design, Layout, and Simulation *John Wiley & Sons*

Praise for CMOS: Circuit Design, Layout, and Simulation Revised Second Edition from the Technical Reviewers "A refreshing industrial flavor. Design concepts are presented as they are needed for 'just-in-time' learning. Simulating and designing circuits using SPICE is emphasized with literally hundreds of examples. Very few textbooks contain as much detail as this one. Highly recommended!" --Paul M. Furth, New Mexico State University "This book builds a solid knowledge of CMOS circuit design from the ground up. With coverage of process integration, layout, analog and digital models, noise mechanisms, memory circuits, references, amplifiers, PLLs/DLLs, dynamic circuits, and data converters, the text is an excellent reference for both experienced and novice designers alike." --Tyler J. Gomm, Design Engineer, Micron Technology, Inc. "The Second Edition builds upon the success of the first with new chapters that cover additional material such as oversampled converters and non-volatile memories. This is becoming the de facto standard textbook to have on every analog and mixed-signal designer's bookshelf." --Joe Walsh, Design Engineer, AMI Semiconductor CMOS circuits from design to implementation CMOS: Circuit Design, Layout, and Simulation, Revised Second Edition covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. Features include: Updated materials to reflect CMOS technology's movement into nanometer sizes Discussions on phase- and delay-locked loops, mixed-signal circuits, data converters, and circuit noise More than 1,000 figures, 200 examples, and over 500 end-of-chapter problems In-depth coverage of both analog and digital circuit-level design techniques Real-world process parameters and design rules The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid learning

Brutal

CMOS Digital Integrated Circuits

Analysis and Design *McGraw-Hill Science, Engineering & Mathematics*

The second edition of this comprehensive text contains extensive revisions to reflect recent advances in technology and in circuit design practices. Recognizing that the area of digital integrated circuit design is evolving at an increasingly fast pace, every effort has been made to present state-of-the-art material on all subjects covered in the book. This book is primarily designed as a comprehensive text for senior level and first-year graduate level digital circuit design classes, as well as a reference for practicing engineers in the areas of IC design and VLSI.

Analog Integrated Circuit Design

The 2nd Edition of Analog Integrated Circuit Design focuses on more coverage about several types of circuits that have increased in importance in the past decade. Furthermore, the text is enhanced with material on CMOS IC device modeling, updated processing layout and expanded coverage to reflect technical innovations. CMOS devices and circuits have more influence in this edition as well as a reduced amount of text on BiCMOS and bipolar information. New chapters include topics on frequency response of analog ICs and basic theory of feedback amplifiers.

High Performance Integrated Circuit Design *McGraw Hill Professional*

The latest techniques for designing robust, high performance integrated circuits in nanoscale technologies Focusing on a new technological paradigm, this practical guide describes the interconnect-centric design methodologies that are now the major focus of nanoscale integrated circuits (ICs). High Performance Integrated Circuit Design begins by discussing the dominant role of on-chip interconnects and provides an overview of technology scaling. The book goes on to cover data signaling, power management, synchronization, and substrate-aware design. Specific design constraints and methodologies unique to each type of interconnect are addressed. This comprehensive volume also explains the design of specialized circuits such as tapered buffers and repeaters for data signaling, voltage regulators for power management, and phase-locked loops for synchronization. This is an invaluable resource for students, researchers, and engineers working in the area of high performance ICs. Coverage includes: Technology scaling Interconnect modeling and extraction Signal propagation and delay analysis Interconnect coupling noise Global signaling Power generation Power distribution networks CAD of power networks Techniques to reduce power supply noise Power dissipation Synchronization theory and tradeoffs Synchronous system characteristics On-chip clock generation and distribution Substrate noise in mixed-signal ICs Techniques to reduce substrate noise

CMOS Circuit Design - Analog, Digital, IC Layout

Electrical and Electronic Engineering Design Series Vol 3 CMOS Circuit Design - Analog, digital, IC Layout This university level Electrical Engineering text is for anyone who wants to know how to design products using CMOS circuits. The present text is unusually accessible to readers who want to acquire the skills of CMOS circuit design as well as the skill making Integrated Circuit Chip Layouts. We present a thorough foundation so that you can proceed to learn how to design and layout CMOS circuits. This

text is different from other CMOS design texts, because not only do we actually show how to design CMOS circuits selecting transistor Length, Width and the correct value of mobility (a small detail that is usually overlooked if not ignored) we show how to make accurate, functioning circuit layouts that can be used in a chip. Furthermore we ask you to work hard drawing over 60 layouts that give you real world experience. This is not about logic design. CMOS technology is the preferred technology for implementing modern digital and analog integrated circuits. We show, step by step, how layouts are made that conform to Mosis rules. A brief review of MOS transistors sets the stage for CMOS circuit design. Digital circuits with no memory implement logic equations as sums of minterms (OR of ANDs) or products of maxterms (AND of ORs). We show how to design circuits such as NOT (Inverter), NAND, NOR, XOR, Multiplexer, and Adder. As we proceed we show how to plan and execute layouts for each circuit. One bit digital circuits with memory are used in state machines. The RS Latch is the most elementary one-bit circuit with memory. Latches do not have clock inputs, whereas flip-flops and edge triggered flip-flops are one-bit memory circuits with clock inputs. The flip-flops are synchronous circuits. We show how to design and layout the RS Latch and the D edge triggered flip-flop. We show that the JK design and layout is a straightforward adaptation of the D design and layout. The D and JK edge triggered flip-flops are the flip-flop circuits in commercial use today. Next the emphasis is on digital circuits that are an assembly of identical cells, such as the cell of a shift register. The integrated circuit layout of an assembly of cells is an orderly, repetitive pattern. Orderly, repetitive patterns are intrinsically free of layout errors. We say orderly layouts are mandatory for non trivial circuits (random logic layouts are high risk). We show how to make orderly systematic layouts, and how to write Spice programs that evaluate their performance. We design and layout well known digital circuits such as shift registers, storage registers with load control, registers on a bus, and programmable logic arrays of logic with no memory. The well known current mirror, differential amplifier, operational amplifier, resistors and capacitors are designed and their performance is evaluated by Spice. Layout procedures for the circuits as well as the resistors and capacitors are presented. Spice is used to plot DC response, AC frequency response, and TRAN transient response performance of circuits that are analyzed and designed in the text. We show how to write these programs. We ask you to draw over 60 layouts, which we consider to be useful experiments that give you real world experience. We consider drawing the more than 60 layouts to be a significant learning activity. The presentations are eminently clear, because they are based on the policies assume nothing and nothing is obvious. The present text's contents are topics one actually uses when engaged in CMOS circuit analysis and design.

Digital Integrated Circuit Design

Integrated circuits (ICs) are a keystone of modern electronics. They are the heart and brains of most circuits, encompassing the particular logic and circuit design techniques required to design integrated circuits, or ICs. ICs consist of miniaturized electronic components built into an electrical network on a monolithic semiconductor substrate by photolithography. Today, due to the continuous miniaturization of electronic components, a single integrated circuit (IC) contains many transistors and interconnections very close each other, and this causes an increased number of unwanted interactions. In a mixed-signal System-on-Chip (SoC), i.e., when analog and digital circuits are integrated on the same silicon chip, performance limitations come mainly from the analog section which interfaces the digital

processing core with the external world. In such ICs, the digital switching activity may affect the analog section. A method to isolate the individual components formed in the substrate is necessary since the substrate silicon is conductive and often forms an active region of the individual components. With the progress of science and technology, communication products play an increasingly important role in the development of countries and improvement of daily life, and the integrated circuits are the core components of communication products. This book entitled "Digital Integrated Circuit Design" is aimed to cover trends and developments in the design and application of analog, radio frequency (RF), and mixed signal integrated circuits (ICs) as well as signal processing circuits and systems. It features both new research results and reviews and reflects the large volume of cutting-edge research activity in this field today. This book intends to mainly introduce the failure analysis technology and process of integrated circuits applied in the communication products. This book also introduces the specific process of failure analysis, and the process can reflect the application of concrete failure analysis method. The integrated circuit failure analysis depends on the accurate confirmation and analysis of chip failure mode, the search of the root failure cause, the summary of failure mechanism and the implement of the improvement measures.

Foundations of Analog and Digital Electronic Circuits *Elsevier*

Unlike books currently on the market, this book attempts to satisfy two goals: combine circuits and electronics into a single, unified treatment, and establish a strong connection with the contemporary world of digital systems. It will introduce a new way of looking not only at the treatment of circuits, but also at the treatment of introductory coursework in engineering in general. Using the concept of "abstraction," the book attempts to form a bridge between the world of physics and the world of large computer systems. In particular, it attempts to unify electrical engineering and computer science as the art of creating and exploiting successive abstractions to manage the complexity of building useful electrical systems. Computer systems are simply one type of electrical systems. +Balances circuits theory with practical digital electronics applications. +Illustrates concepts with real devices. +Supports the popular circuits and electronics course on the MIT OpenCourse Ware from which professionals worldwide study this new approach. +Written by two educators well known for their innovative teaching and research and their collaboration with industry. +Focuses on contemporary MOS technology.

Physical Design of CMOS Integrated Circuits Using L-Edit *Pws Publishing Company*

"Physical Design of CMOS Integrated Circuits Using L-Edit is the first book/software package that enables engineering students and professionals to perform full IC layout on an inexpensive personal computer. The Student Version of L-Edit, included with the book on a 3.5-inch disk, is a full-featured layout editor that runs on MS-DOS compatible computers with minimal hardware requirements (640K RAM, a mouse, and an EGA or better color monitor). L-Edit allows the user to implement the physical design of an integrated circuit at the silicon level, and provides output for circuit simulation on SPICE. The entire process of chip design - once the exclusive province of workstation-based CAD systems - can now be performed on a PC." "Database files for many standard MOSIS CMOS processes are provided on disk, including Orbit and HP 2.0 and 1.2-micron technology base definitions. The program provides for circuit extraction (translating the layout to a SPICE-compatible text file), and design rule checking using

predefined MOSIS rules or custom-designed sets. It also features a unique cross-sectional viewer that constructs the side view layering from the layout; this viewer helps users visualize the link between layout drawings and the device structure. Circuit designs created on the Student Version of L-Edit can be translated to GDS II or CIF format for submission to a fabrication foundry using the Professional Version of L-Edit."--BOOK JACKET. Title Summary field provided by Blackwell North America, Inc. All Rights Reserved

Variation-Aware Design of Custom Integrated Circuits: A Hands-on Field Guide *Springer Science & Business Media*

This book targets custom IC designers who are encountering variation issues in their designs, especially for modern process nodes at 45nm and below, such as statistical process variations, environmental variations, and layout effects. It teaches them the state-of-the-art in Variation-Aware Design tools, which help the designer to analyze quickly the variation effects, identify the problems, and fix the problems. Furthermore, this book describes the algorithms and algorithm behavior/performance/limitations, which is of use to designers considering these tools, designers using these tools, CAD researchers, and CAD managers.

Integrated Circuit Design and Technology *Springer*

Electronic Design Automation for IC System Design, Verification, and Testing *CRC Press*

The first of two volumes in the Electronic Design Automation for Integrated Circuits Handbook, Second Edition, Electronic Design Automation for IC System Design, Verification, and Testing thoroughly examines system-level design, microarchitectural design, logic verification, and testing. Chapters contributed by leading experts authoritatively discuss processor modeling and design tools, using performance metrics to select microprocessor cores for integrated circuit (IC) designs, design and verification languages, digital simulation, hardware acceleration and emulation, and much more. New to This Edition: Major updates appearing in the initial phases of the design flow, where the level of abstraction keeps rising to support more functionality with lower non-recurring engineering (NRE) costs Significant revisions reflected in the final phases of the design flow, where the complexity due to smaller and smaller geometries is compounded by the slow progress of shorter wavelength lithography New coverage of cutting-edge applications and approaches realized in the decade since publication of the previous edition—these are illustrated by new chapters on high-level synthesis, system-on-chip (SoC) block-based design, and back-annotating system-level models Offering improved depth and modernity, Electronic Design Automation for IC System Design, Verification, and Testing provides a valuable, state-of-the-art reference for electronic design automation (EDA) students, researchers, and professionals.

Single Flux Quantum Integrated Circuit Design *Springer Nature*

Design of Analog CMOS Integrated Circuits *Tata McGraw-Hill Education*

Introduction to Microelectronic Fabrication

This introductory book assumes minimal knowledge of the existence of integrated circuits and of the terminal behavior of electronic components such as resistors, diodes, and MOS and bipolar transistors. It presents to readers the basic information necessary for more advanced processing and design books.

Focuses mainly on the basic processes used in fabrication, including lithography, oxidation, diffusion, ion implementation, and thin film deposition. Covers interconnection technology, packaging, and yield. Appropriate for readers interested in the area of fabrication of solid state devices and integrated circuits.

Analysis and Design of Analog Integrated Circuits, 5th Edition *Wiley Global Education*

This is the only comprehensive book in the market for engineers that covers the design of CMOS and bipolar analog integrated circuits. The fifth edition retains its completeness and updates the coverage of bipolar and CMOS circuits. A thorough analysis of a new low-voltage bipolar operational amplifier has been added to Chapters 6, 7, 9, and 11. Chapter 12 has been updated to include a fully differential folded cascode operational amplifier example. With its streamlined and up-to-date coverage, more engineers will turn to this resource to explore key concepts in the field.

Design of Terahertz CMOS Integrated Circuits for High-Speed Wireless Communication *Materials, Circuits and Device*

This book describes recent research on terahertz CMOS design for high-speed wireless communication. The topics covered include fundamental technologies for terahertz CMOS design, amplifier design, physical design approaches, transceiver design, and future prospects.

CMOS VLSI Design: A Circuits and Systems Perspective *Pearson Education India*

Fast Techniques for Integrated Circuit Design *Cambridge University Press*

Learn how to use estimation techniques to solve real-world IC design problems and accelerate design processes with this practical guide.

The Experience Economy *Harvard Business Press*

Rev. ed. of: The experience economy: work is theatre & every business a stage. 1999.

Low Power Design Methodologies *Springer Science & Business Media*

Low Power Design Methodologies presents the first in-depth coverage of all the layers of the design hierarchy, ranging from the technology, circuit, logic and architectural levels, up to the system layer. The book gives insight into the mechanisms of power dissipation in digital circuits and presents state of the art approaches to power reduction. Finally, it introduces a global view of low power design methodologies and how these are being captured in the latest design automation environments. The individual chapters are written by the leading researchers in the area, drawn from both industry and academia. Extensive references are included at the end of each chapter. Audience: A broad introduction for anyone interested in low power design. Can also be used as a text book for an advanced graduate class. A starting point for any aspiring researcher.

Digital Circuit Design

Principles and Practice

The modern world is overrun with electronic equipment, handling huge quantities of data. At the heart of this scenario lies the digital circuitry, which provides the powerful intelligence needed. Thus, there is an increasing need for design engineers in this expanding area. This text starts from basic ideas of logical gates,

and progresses through to advanced concepts of digital systems. Each chapter comes with a wealth of illustrative examples and assignment questions for lecture-room use. Contents List of Digital Circuit Design Chapter 1 Introduction to Digital Systems and Logic Gates 1.1 The transition from analogue to digital signals 1.2 Digital logic levels 1.3 The concept of gates 1.4 The AND gate 1.5 The OR gate 1.6 The XOR gate (Exclusive-OR) 1.7 The NOT gate 1.8 Bubbled gates 1.9 The NOR gate 1.10 The NAND gate 1.11 The XNOR gate Chapter 2 Boolean Algebra 2.1 Introducing Boolean algebra 2.2 The AND operation in Boolean algebra 2.3 The OR operation in Boolean algebra 2.4 The XOR operation in Boolean algebra 2.5 The NOT function in Boolean algebra 2.6 Examples of Boolean calculations 2.7 Theorems of Boolean algebra Chapter 3 Combinational Logic 3.1 Illustrations of combinational logic 3.2 Developing Boolean expressions for combinational circuits 3.3 The importance of minimisation 3.4 Karnaugh maps (K-maps) 3.5 Summary of K-map looping rules 3.6 "Can't Happen" states 3.7 Static hazards Chapter 4 Number Systems 4.1 Types of numerical system 4.2 The Decimal number system 4.3 The Binary system 4.4 Binary-to-Decimal conversion 4.5 Decimal-to-binary conversion 4.6 Binary operations 4.7 The Hexadecimal number system Chapter 5 Adders, Subtractors and Multipliers 5.1 Arithmetic in digital circuits 5.2 The half adder 5.3 The full adder 5.4 The parallel binary adder (Ripple carry parallel adder) 5.5 The half subtractor 5.6 The full subtractor 5.7 Multipliers Chapter 6 Multiplexers and Decoders 6.1 Comparators 6.2 Multiplexers 6.3 Demultiplexers 6.4 Encoders 6.5 Decoders Chapter 7 Latches and Flip-Flops 7.1 Introducing time into logic circuits 7.2 The bistable multivibrator (Flip-flop) 7.3 The SR latch 7.4 The SR flip-flop 7.5 The T-type flip-flop 7.6 The D-type flip-flop (Data latch) 7.7 The JK flip-flop 7.8 The Master-Slave JK flip-flop 7.9 Preset and Clear inputs 7.10 Integrated circuit flip-flops Chapter 8 Shift Registers 8.1 Basic shift register functions 8.2 Serial-in serial-out shift registers 8.3 Serial-in parallel-out shift registers 8.4 Parallel-in serial-out shift registers 8.5 Parallel-in parallel-out shift registers 8.6 Bidirectional shift registers 8.7 Shift register counters Chapter 9 Multivibrators and Timers 9.1 What are multivibrators? 9.2 Astable multivibrators 9.3 The monostable multivibrator 9.4 The 555 timer 9.5 Applications of the 555 timer Chapter 10 Counters 10.1 Introducing counters 10.2 Asynchronous counter operation 10.3 Synchronous counter operation 10.4 Up/down synchronous counters 10.5 Cascaded counters 10.6 Counter decoding 10.7 Counter applications conversion Chapter 11 Memories and Data Storage 11.1 Memory types 11.2 Classification by fabrication technology 11.3 Memory terminology 11.4 ROM (Read-Only Memory) 11.5 RAM (Random-Access Memory) Chapter 12 Design of Digital Integrated Circuits (ICs) 12.1 Logic families 12.2 Electrical characteristics of digital ICs margin 12.3 RTL and DTL families 12.4 The TTL logic family 12.5 The ECL logic family 12.6 The I²L logic family 12.7 The MOSFET logic family 12.8 CMOS circuits gates

Electronic Circuits

Fundamentals and Applications *Routledge*

Electronics explained in one volume, using both theoretical and practical applications. Mike Tooley provides all the information required to get to grips with the fundamentals of electronics, detailing the underpinning knowledge necessary to appreciate the operation of a wide range of electronic circuits, including amplifiers, logic circuits, power supplies and oscillators. The 5th edition includes an additional chapter showing how a wide range of useful electronic applications can be developed in conjunction with the increasingly popular Arduino microcontroller, as well as a new section on batteries for use in electronic equipment and

some additional/updated student assignments. The book's content is matched to the latest pre-degree level courses (from Level 2 up to, and including, Foundation Degree and HND), making this an invaluable reference text for all study levels, and its broad coverage is combined with practical case studies based in real-world engineering contexts. In addition, each chapter includes a practical investigation designed to reinforce learning and provide a basis for further practical work. A companion website at <http://www.key2electronics.com> offers the reader a set of spreadsheet design tools that can be used to simplify circuit calculations, as well as circuit models and templates that will enable virtual simulation of circuits in the book. These are accompanied by online self-test multiple choice questions for each chapter with automatic marking, to enable students to continually monitor their own progress and understanding. A bank of online questions for lecturers to set as assignments is also available.

Microelectronics *Wiley*

By helping students develop an intuitive understanding of the subject, Microelectronics teaches them to think like engineers. The second edition of Razavi's Microelectronics retains its hallmark emphasis on analysis by inspection and building students' design intuition, and it incorporates a host of new pedagogical features that make it easier to teach and learn from, including: application sidebars, self-check problems with answers, simulation problems with SPICE and MULTISIM, and an expanded problem set that is organized by degree of difficulty and more clearly associated with specific chapter sections.

An Introduction to Digital and Analog Integrated Circuits and Applications *Intex Educational Pub*

Designing Analog Chips *Virtualbookworm Publishing*

A comprehensive introduction to CMOS and bipolar analog IC design. The book presumes no prior knowledge of linear design, making it comprehensible to engineers with a non-analog background. The emphasis is on practical design, covering the entire field with hundreds of examples to explain the choices. Concepts are presented following the history of their discovery. Content: 1. Devices Semiconductors, The Bipolar Transistor, The Integrated Circuit, Integrated NPN Transistors, The Case of the Lateral PNP Transistor, CMOS Transistors, The Substrate PNP Transistor, Diodes, Zener Diodes, Resistors, Capacitors, CMOS vs. Bipolar; 2. Simulation, DC Analysis, AC Analysis, Transient Analysis, Variations, Models, Diode Model, Bipolar Transistor Model, Model for the Lateral PNP Transistor, MOS Transistor Models, Resistor Models, Models for Capacitors; 3. Current Mirrors; 4. Differential Pairs; 5. Current Sources; 6. Time Out: Analog Measures, dB, RMS, Noise, Fourier Analysis, Distortion, Frequency Compensation; 7. Bandgap References; 8. Op Amps; 9. Comparators; 10. Transimpedance Amplifiers; 11. Timers and Oscillators; 12. Phase-Locked Loops; 13. Filters; 14. Power, Linear Regulators, Low Drop-Out Regulators, Switching Regulators, Linear Power Amplifiers, Switching Power Amplifiers; 15. A to D and D to A, The Delta-Sigma Converter; 16. Odds and Ends, Gilbert Cell, Multipliers, Peak Detectors, Rectifiers and Averaging Circuits, Thermometers, Zero-Crossing Detectors; 17. Layout.

Computer Architecture

A Quantitative Approach *Morgan Kaufmann*

Computer Architecture: A Quantitative Approach, Sixth Edition has been considered essential reading by instructors, students and practitioners of computer design for over 20 years. The sixth edition of this classic textbook from Hennessy and Patterson, winners of the 2017 ACM A.M. Turing Award recognizing contributions of lasting and major technical importance to the computing field, is fully revised with the latest developments in processor and system architecture. The text now features examples from the RISC-V (RISC Five) instruction set architecture, a modern RISC instruction set developed and designed to be a free and openly adoptable standard. It also includes a new chapter on domain-specific architectures and an updated chapter on warehouse-scale computing that features the first public information on Google's newest WSC. True to its original mission of demystifying computer architecture, this edition continues the longstanding tradition of focusing on areas where the most exciting computing innovation is happening, while always keeping an emphasis on good engineering design. Winner of a 2019 Textbook Excellence Award (Texty) from the Textbook and Academic Authors Association Includes a new chapter on domain-specific architectures, explaining how they are the only path forward for improved performance and energy efficiency given the end of Moore's Law and Dennard scaling Features the first publication of several DSAs from industry Features extensive updates to the chapter on warehouse-scale computing, with the first public information on the newest Google WSC Offers updates to other chapters including new material dealing with the use of stacked DRAM; data on the performance of new NVIDIA Pascal GPU vs. new AVX-512 Intel Skylake CPU; and extensive additions to content covering multicore architecture and organization Includes "Putting It All Together" sections near the end of every chapter, providing real-world technology examples that demonstrate the principles covered in each chapter Includes review appendices in the printed text and additional reference appendices available online Includes updated and improved case studies and exercises ACM named John L. Hennessy and David A. Patterson, recipients of the 2017 ACM A.M. Turing Award for pioneering a systematic, quantitative approach to the design and evaluation of computer architectures with enduring impact on the microprocessor industry

CMOS Digital Integrated Circuits Analysis & Design *McGraw-Hill Education*

The fourth edition of CMOS Digital Integrated Circuits: Analysis and Design continues the well-established tradition of the earlier editions by offering the most comprehensive coverage of digital CMOS circuit design, as well as addressing state-of-the-art technology issues highlighted by the widespread use of nanometer-scale CMOS technologies. In this latest edition, virtually all chapters have been re-written, the transistor model equations and device parameters have been revised to reflect the significant changes that must be taken into account for new technology generations, and the material has been reinforced with up-to-date examples. The broad-ranging coverage of this textbook starts with the fundamentals of CMOS process technology, and continues with MOS transistor models, basic CMOS gates, interconnect effects, dynamic circuits, memory circuits, arithmetic building blocks, clock and I/O circuits, low power design techniques, design for manufacturability and design for testability.