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You acquire the image using the CPU and then send it to the FPGA via direct memory access (DMA) so the FPGA can perform operations such as filtering or color plane extraction. Then you can send the image back to the CPU for more advanced operations such as optical character recognition (OCR) or pattern matching.

CPU or FPGA for image processing: Which is best? | Vision ...

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Intel FPGAs offer both volatile and non-volatile key storage. The key is stored in FPGAs when using the design security feature. Depending on the security mode, you can configure the FPGAs with a configuration file that is encrypted with the same key, or for board testing, configure with a normal configuration file.

AN 556: Using the Design Security

Features in Intel FPGAs

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Managing Security in FPGA-Based Embedded Systems

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similar to that used for an application-specific integrated circuit (ASIC). Circuit diagrams were previously used to specify ...

Field-programmable gate array - Wikipedia

This is another great application for FPGA based image processing, the ability to work with the sensors for long integration times and process and store the image is a real benefit. Of course in a real system, you might even stop the majority of the FPGA clocks while the image is being captured to prevent the additional noise from being injected into the captured image.

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Image: ZDNet, Priyanka from the Noun Project In a new research paper published on the last day of 2019, a

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