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channels and 100nm CMOS gates, etch undercutting target may be 10nm or 10µm, but it is there; dopants will diffuse during high temperature anneals, but the junction depth target may be tens of nanometres or tens of micrometres.

Introduction to Microfabrication

10.1 Introduction In microelectronic fabrication, metals are used as electrically conducting materials for interconnects. The properties such as resistivity, lithography process compatibility, melting point, thermal expansion coefficient, diffusivity in an insulator, adhesion to the insulator, and interatomic distances (stress) of the metal must be considered when selecting an appropriate interconnect material.

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